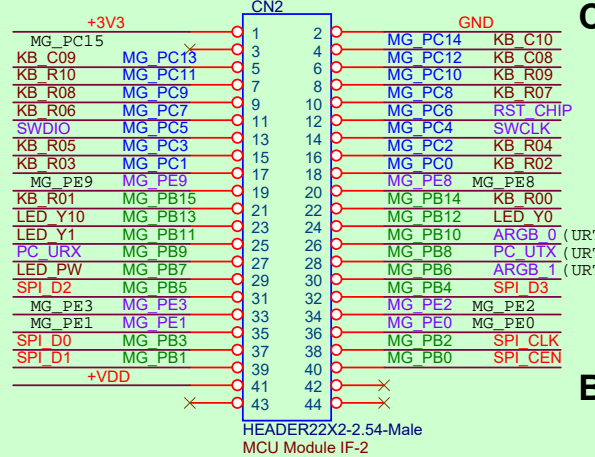
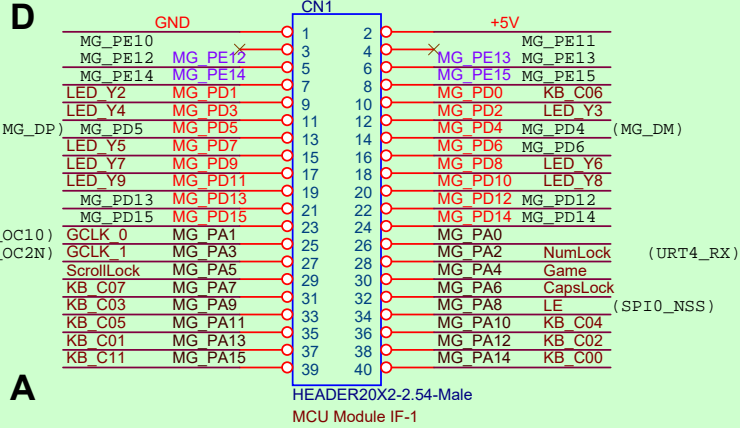
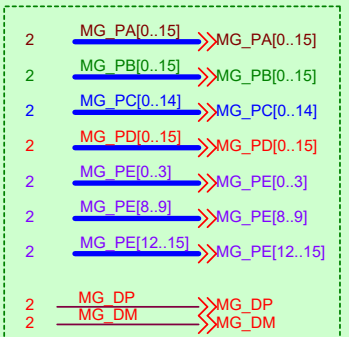
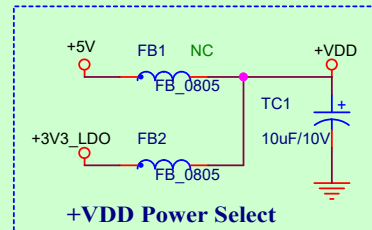
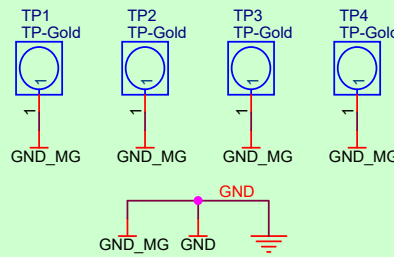
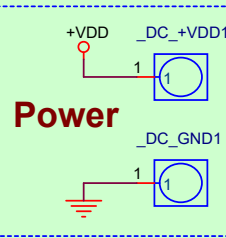
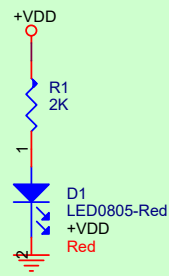


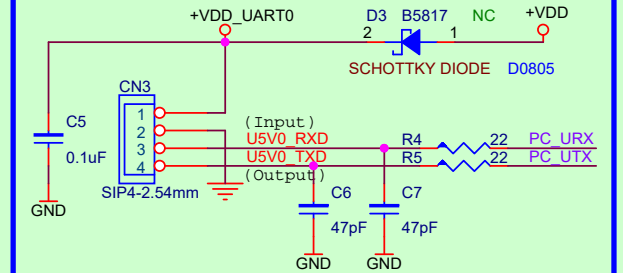
# MG07-01B

2023.7 GR09-2308A  
MG32 LQFP80 PKT84  
+USB Connector

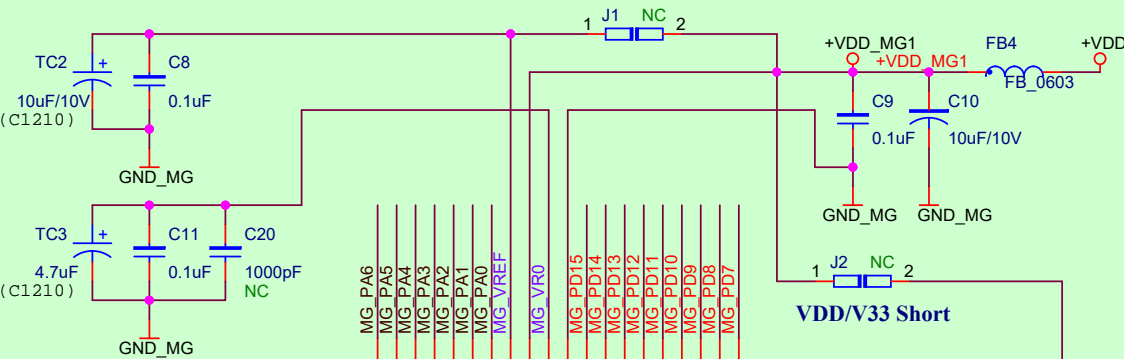


G1  
MEGAWIN  
Logo\_Megawin10+Text  
NC

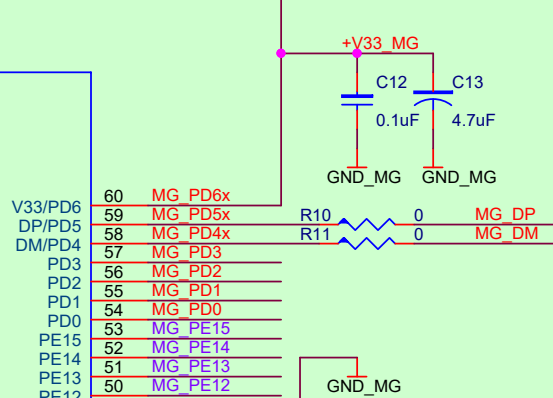
## UART Connector



## VDD/VREF+ Short

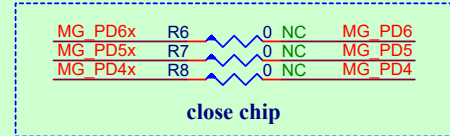
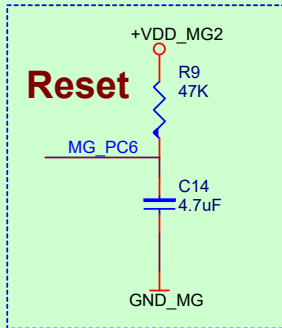
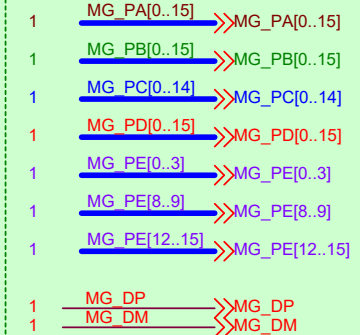


## VDD/V33 Short



## ECO List on Circuit (from MG07-01A):

1. Add Q1/R14/R15/C19 USB power delay circuit
2. Add UP2/UP3/R16 optional LDO circuit
3. Add C20 circuit



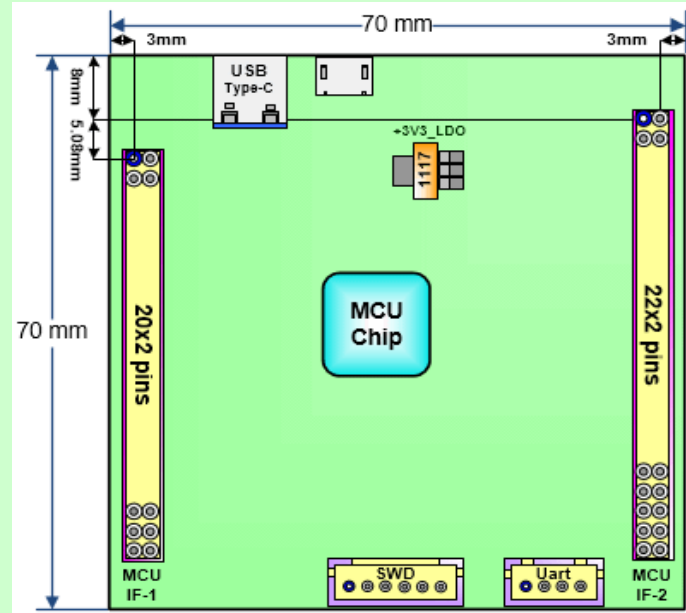
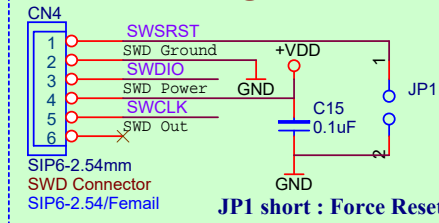
## MG32F02U\_LQFP80 (2019\_1226)

MG PA7	1	PA7/ADC7
MG PA8	2	PA8/ADC8
MG PA9	3	PA9/ADC9
MG PA10	4	PA10/ADC10
MG PA11	5	PA11/ADC11
MG PA12	6	PA12/ADC12
MG PA13	7	PA13/ADC13
MG PA14	8	PA14/ADC14
MG PA15	9	PA15/ADC15
MG PB0	10	PB0/NSS
MG PB1	11	PB1/MISO
MG PB2	12	PB2/SCK
MG PB3	13	PB3/MOSI
MG PE0	14	PE0/OBM10
MG PE1	15	PE1/OBM11
MG PE2	16	PE2
MG PE3	17	PE3/OBMP1
MG PB4	18	PB4/SD3
MG PB5	19	PB5/SD2
MG PB6	20	PB6

V33/PD6	60	MG PD6x
DP/PD5	59	MG PD5x
DM/PD4	58	MG PD4x
PD3	57	MG PD3
PD2	56	MG PD2
PD1	55	MG PD1
PD0	54	MG PD0
PE15	53	MG PE15
PE14	52	MG PE14
PE13	51	MG PE13
PE12	50	MG PE12
VSS1	49	VSS1
XOUT/PC14	48	MG PC14
XIN/PC13	47	MG PC13
PC12	46	MG PC12
SDA1/PC11	45	MG PC11
SCL1/PC10	44	MG PC10
RXD1/PC9	43	MG PC9
TXD1/PC8	42	MG PC8
PC7	41	MG PC7

MG_PC4	SWCLK
MG_PC5	SWDIO
MG_PC6	SWSRST

## SWD



megawin		
Title		
MCU Chip		
Size C	Document Number	Rev
	MG07-01	1.0
Date:	Monday, April 29, 2024	Sheet 2 of 2